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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/839,679 04/20/2001		Gary J. Sullivan	MS1-601US	1812	
22801	7590 03/24/2006		EXAMINER		
LEE & HAY		CZEKAJ, DAVID J			
421 W RIVER SPOKANE, V	RSIDE AVENUE SUITE WA 99201	500	ART UNIT	PAPER NUMBER	
,			2621	-	

DATE MAILED: 03/24/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

			Application	No.	Applicant(s)				
Office Action Summary		09/839,679	09/839,679 SULLIVAN, GARY		ſ J.				
		Examiner		Art Unit					
			Dave Czeka	*	2616				
Period fo	The MAILING DATE of this commun or Reply	ication appe	ears on the d	cover sheet with the c	orrespondence ad	ldress			
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD F. HEVER IS LONGER, FROM THE M sions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this comm period for reply is specified above, the maximum stare to reply within the set or extended period for reply eply received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	IAILING DA of 37 CFR 1.136 nunication. atutory period will will, by statute, o	TE OF THIS  (a). In no event  Il apply and will cause the applica	S COMMUNICATION, however, may a reply be time expire SIX (6) MONTHS from the ation to become ABANDONEI	l. ely filed the mailing date of this c O (35 U.S.C.§ 133).				
Status									
1)	Responsive to communication(s) file	ed on <u>26 Oc</u>	tober 2005.						
,—	•	2b) This a		n-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is								
	closed in accordance with the practi	ce under Ex	k parte Qua	yle, 1935 C.D. 11, 45	3 O.G. 213.				
Dispositi	on of Claims								
4)⊠	4)⊠ Claim(s) <u>1-25</u> is/are pending in the application.								
	4a) Of the above claim(s) is/are withdrawn from consideration.								
5) 🗌	Claim(s) is/are allowed.								
6)⊠	Claim(s) <u>1-25</u> is/are rejected.								
•	Claim(s) is/are objected to.								
8) 🗌	Claim(s) are subject to restrict	ction and/or	election red	quirement.					
Applicati	on Papers								
9)□	The specification is objected to by th	e Examiner.							
10)[	The drawing(s) filed on is/are:	: a) <u></u> acce	pted or b)	objected to by the E	Examiner.				
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).									
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	The oath or declaration is objected to	o by the Exa	aminer. Not	e the attached Office	Action or form P	ΓO-152.			
Priority (	ınder 35 U.S.C. § 119								
a)	Acknowledgment is made of a claim  All b) Some * c) None of:  1. Certified copies of the priority  2. Certified copies of the priority  3. Copies of the certified copies application from the Internationsee the attached detailed Office actions	documents documents of the prioritional Bureau	have been have been ity documer (PCT Rule	received. received in Applicati its have been receive 17.2(a)).	on No ed in this National	Stage			
2) Notice 3) Information	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (F mation Disclosure Statement(s) (PTO-1449 or r No(s)/Mail Date <u>9/16/05</u> .			4)  Interview Summary Paper No(s)/Mail Da 5)  Notice of Informal P 6)  Other:	ite	O-152)			

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#### **DETAILED ACTION**

## Response to Arguments

Applicant's arguments with respect to claims 1-25 have been considered but are most in view of the new ground(s) of rejection.

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over MacInnis et al. (6744472), (hereinafter referred to as "MacInnis") in view of Richter et al. (6725279), (hereinafter referred to as "Richter") in further view of Sriram et al. (6539059), (hereinafter referred to as "Sriram").

Regarding claims 1-2, 10-12, 18, and 24-25, MacInnis discloses an apparatus that relates to an integrated circuit graphics display system (MacInnis: column 1, lines 41-43). This apparatus comprises "receiving a command from a decoder application" (MacInnis: figure 2, item 50, wherein the decoder application is the video decoder) and "generating one or more filter control command data structures recognizable by a communicatively coupled accelerator including one or more parameters which affect one or more filter settings of the accelerator" (MacInnis: figure 2, column 57, lines 21-37, wherein the filter parameters are the blending, scaling, blitting, and filling, the accelerator is the graphics accelerator). Although MacInnis fails to explicitly show an

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application interface in MacInnis's figures, the examiner notes that the system depicted in figure 1 would require an interface to correctly operate. MacInnis further fails to show the API configured to facilitate the use of a plurality of accelerators. Richter teaches that using an application interface provides a flexible system allowing the use of complex processing configurations such as encoders and decoders (Richter: column 4. lines 20-31). Sriram teaches that there is a need for a efficiently scalable decoder which facilitates efficiency, synchronization, flexibility and functionality (Sriram: column 2, lines 59-64). To help alleviate this problem, Sriram discloses an API that "is configured to facilitate the use of a plurality of different multimedia accelerators with the decoder application" (Sriram: column 4, lines 48-54, wherein the accelerators are the sub-processors; column 7, lines 10-14, column 8, lines 1-14, wherein the interface or API is the monitor processor) and "wherein the decoder application is configured to iteratively issue configuration commands reflecting various decoding acceleration capabilities until choosing one that is acceptable to both the decoder and accelerator" (Sriram: column 5, lines 58-67, column 12, lines 59-63, wherein the configuration commands is the parameter passing). Therefore, the combined teaching of MacInnis, Richter, and Sriram as a whole would have rendered obvious to one having ordinary skill in the art at the time the invention was made to implement an application interface taught by Richter, and add the API configuration taught by Sriram in order to obtain an apparatus that is more versatile by being able to perform complex operations.

Regarding claims 3, and 20, MacInnis discloses "the filter is a post-processing filter" (MacInnis: figure 28).

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Regarding claim 4, Richter discloses "output data subsequent to the application of a post-processing filter are used as prediction references" (Richter: column 4, lines 29-31, wherein filters and prediction references are well known within the environment of encoders and decoders).

Regarding claims 5, 14, and 21, MacInnis discloses "the post processing filter is a de-ringing filter" (MacInnis: column 9, lines 52-58, wherein low pass filtering requires the signal to be de-rung).

Regarding claims 6-7, 17, and 23, MacInnis discloses "the parameters include a strength parameter" (MacInnis: column 4, lines 40-51, wherein the strength parameter is the scaling).

Regarding claims 8-9, 15-16, and 22, MacInnis discloses "the API issues control commands for 4 or 16 luminance structures and/or 2, 4, 8, 16, or 32 chrominance structures" (MacInnis: column 9, lines 34-44, wherein the YUV converter uses the above chrominance and luminance structures).

Regarding claim 13, MacInnis discloses "the filter control structures effect one or more of the post processing filters" (MacInnis: figure 2, column 57, lines 21-37, wherein the filter structures indicate whether to blend, scale, blitte, and/or fill).

Regarding claim 19, MacInnis in view of Richter disclose "one ore more media accelerators coupled to the decoder application via the API" (MacInnis: figures 1-2, wherein the accelerator is the graphics accelerator, the decoder application is the video decoder; Richter: column 4, lines 20-31).

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### Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

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12-1999

Bheda et al.

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dave Czekaj whose telephone number is (571) 272-7327. The examiner can normally be reached on Monday - Friday 9 hours.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mehrdad Dastouri can be reached on (571) 272-7418. The fax phone

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number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DJC

PRIMARY EXAMINER